



Form 1449 (Modified)	Atty Docket No. BEKAP005
<b>Information Disclosure Statement By Applicant</b>	Application No.: 10/003,724
	Inventor ERDOGAN, et al
	Group 2816
	Filing Date November 14, 2001
(Use Several Sheets if Necessary)	

#### U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A						
	B						
	C						
	D						
	E						
	F						

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#### Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
	G						Yes	No

#### Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
mai	H	Behbahani, Farbod, et al., "CMOS Mixers and Polyphase Filters for Large Image Rejection", IEEE Journal of Solid-State Circuits, Vol. 36, No. 6, June 2001, pg. 873.
↓	I	Hornak, Thomas, et al., "An Image-Rejecting Mixer and Vector Filter with 55-dB Image Rejection over Process, Temperature, and Transistor Mismatch, IEEE Journal of Solid-State Circuits, Vol 36, No. 1, January 2001.
	T	
Examiner mai		Date Considered 1/5/05

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.